

Patent

Customer No.: 31561  
Docket No. 11041-US-PA  
Application No.: 10/604,861



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Applicant : Chang et al.  
Application No. : 10/604,861  
Filed : 2003/08/22  
For : FLASH MEMORY CELL AND FABRICATING METHOD  
THEREOF  
Art Unit : 2818  
Examiner : NGUYEN, THINH T

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**TRANSMITTAL LETTER**

**002-1-703-872-9306**

**(Via fax: 4 pages, followed by confirmation copy via courier)**

Assistant Commissioner for Patents  
Arlington, Virginia 22202

Dear Sirs,

In response to the Office Action dated June 24, 2004, please find the relevant paper in response to paper No. 062204. Following the fax transmission, a hard copy via courier will also be forwarded to the Office.

Enclosed documents via courier will include:

- ☒ Response to Restriction Requirement in (2) pages
- ☒ Transmittal letter
- ☒ Fax confirmation report
- ☒ Prepaid return postcard

I believe that no fee is incurred. However, the Commissioner is authorized to charge any fees required in connection with the filing of this paper to account No. 50-2620 (Order No.: 11041-US-PA)

Thank you for your assistance in the subject matter. If you have any questions, please feel free to contact me.

**Patent**

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Docket No. 11041-US-PA  
**Application No.: 10/604,861**

Respectfully Submitted,  
JIANQ CHYUN Intellectual Property Office

Date: July 14, 2004

By: Belinda Lee  
Belinda Lee  
Registration No.: 46,863

**Please send future correspondence to:**

**7F. -1, No. 100, Roosevelt Rd.,**

**Sec. 2, Taipei 100, Taiwan, R.O.C.**

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**RESPONSE TO RESTRICTION REQUIREMENT**

U.S. Patent and Trademark Office  
Commissioner for Patents  
2011 South Clark Place  
Customer Window, Mail Stop Amendment  
Crystal Plaza Two, Lobby, Room 1B03  
Arlington, Virginia 22202

Dear Sirs,

In response to the Office Action, mailed on June 24, 2004, Applicant would like to elect Group I, claims 1-8, drawn to a semiconductor device, classified in class 257, subclass 315. Please cancel Group II, claims 9-19, drawn to process of making a semiconductor device, classified in class 438, subclass 201 without waiver, prejudice or disclaimer.

Thank you for your assistance in the subject matter. If you have any questions, please feel free to contact me.

Respectfully Submitted,  
JIANQ CHYUN IP Office

Date: July 14, 2004

By: Belinda Lee  
Belinda Lee  
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